<22 control bits> <opcode> <cycle\_number> <value\_in\_PC> <value\_in\_SP> <value\_in\_OR> <value\_in\_IR> <R0 R1 R2 R2 R3 R4 R5 R6 R7> <values\_of\_flag\_array\_same\_format\_as\_register\_array> <memory\_address value\_at\_memory\_address>

//That would mean

(1 0 0 1 0 0 1 0 0 1 0 0 1 0 0 0 0 0 0 0 0 0) (1) (12) (ff) (5) (07) (0 1 4 2 3 1 0 8) (0 1 0 1 0 1 0 1) (a6 9 a7 10 a8 11)

Between every two separate things, I would like either a space or a tab as delimiter

Between every two states(or two clock cycles), I would like a '\n' delimiter

Of course, no brackets, that was to make the separation clear, but I do not want brackets.

All addresses in hex, all values in decimal

So finally, a sample will look like -

1 0 0 1 0 0 1 0 0 1 0 0 1 0 0 0 0 0 0 0 0 0 1 12 ff 5 07 0 1 4 2 3 1 0 8 0 1 0 1 0 1 0 1 a6 9 a7 10 a8 11 ...